1. General description

Nexperia introduces leading edge Silicon Carbide (SiC) Schottky diode for ultra high performance, low loss, high efficiency power conversion applications. The SiC Schottky diode encapsulated in a Real-2-Pin D2PAK R2P (TO-263-2) Surface-Mounted Device (SMD) power plastic package offers temperature independent capacitive turn-off, zero recovery switching behavior combined with an outstanding figure-of-merit ($Q_C \times V_F$). The Merged PiN Schottky (MPS) diode improves the robustness expressed in a high I_{FSM} .

2. Features and benefits

- Zero forward and reverse recovery
- Temperature independent fast and smooth switching performance
- Outstanding figure of merit (Q_c x V_F)
- High I_{FSM} capability
- · High power density
- · Reduced system costs
- System miniaturization
- Reduced EMI

3. Applications

- Switch Mode Power Supply (SMPS)
- AC-DC and DC-DC converter
- · Battery charging infrastructure
- Server and telecom power supply
- Uninterruptible Power Supply (UPS)
- Photovoltaic inverters

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit	
I _F	forward current	T _c ≤ 135 °C; δ = 1		-	-	20	Α	
Static characte	Static characteristics							
V _{DC}	DC blocking voltage	T _j = 25 °C		1200	-	-	٧	
Dynamic chara	Dynamic characteristics							
Q_C	total capacitive charge	$V_R = 800 \text{ V}; \text{ dI}_F/\text{dt} = 200 \text{ A/}\mu\text{s}; \text{ I}_F = 20 \text{ A}; $ $T_j = 25 \text{ °C}$		-	85	-	nC	



1200 V, 20 A SiC Schottky diode in D2PAK R2P

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode	mb	
2	Α	anode		
mb	К	mounting base; connected to cathode	1 2 D2PAK R2P (SOT8018)	K K; mb

6. Ordering information

Table 3. Ordering information

Type number	Package				
	Name	Description	Version		
PSC20120J	D2PAK R2P	Plastic, single-ended surface-mounted package (D2PAK R2P); Real-2-Pin configuration; 5.08 mm pitch; 8.8 mm x 10.35 mm x 4.46 mm body	SOT8018		

7. Marking

Table 4. Marking codes

Type number	Marking code
PSC20120J	PSC20120J

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8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{RRM}	repetitive peak reverse voltage	T _j = 25 °C	-	1200	V
l _F	forward current	T _c ≤ 135 °C; δ = 1	-	20	Α
I _{FSM}	non-repetitive peak	t _p = 10 μs; square wave; T _c = 25 °C	-	710	Α
	forward current	t _p = 10 ms; half sine-wave; T _c = 25 °C	-	135	Α
		t_p = 10 ms; half sine-wave; T_c = 150 °C	-	100	Α
∫i ² dt	i ² t value	t _p = 10 ms; T _c = 25 °C	-	91	A²s
		t _p = 10 ms; T _c = 150 °C	-	50	A²s
P _{tot}	total power dissipation	T _c ≤ 25 °C	-	200	W
T _j	junction temperature		-55	175	°C
T _{amb}	ambient temperature		-55	175	°C
T _{stg}	storage temperature		-65	175	°C

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9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
11(J-C)	thermal resistance from junction to case		-	0.6	0.75	K/W

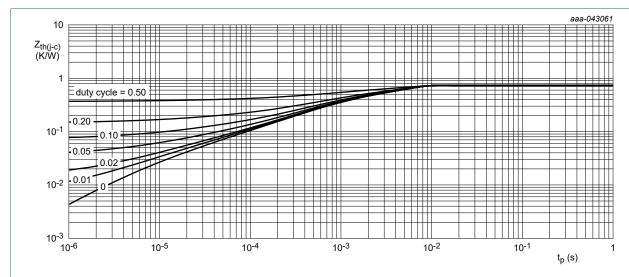


Fig. 1. Transient thermal impedance as a function of pulse duration; maximum values

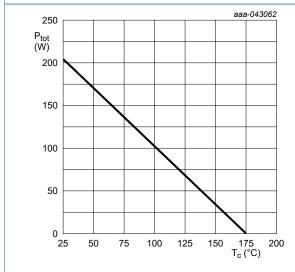


Fig. 2. Power dissipation; maximum values

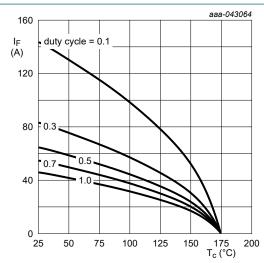


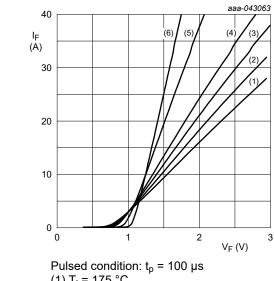
Fig. 3. Forward current as a function of case temperature; maximum values

1200 V, 20 A SiC Schottky diode in D2PAK R2P

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics					
V_{DC}	DC blocking voltage	T _j = 25 °C	1200	-	-	V
V _F	forward voltage	I _F = 20 A; T _j = 25 °C	-	1.5	1.8	V
		I _F = 20 A; T _j = 150 °C	-	2.2	2.6	V
I _R	reverse current	V _R = 1200 V; T _j = 25 °C	-	1	180	μΑ
		V _R = 1200 V; T _j = 150 °C	-	10	500	μΑ
Dynamic ch	naracteristics					
C _d	diode capacitance	V _R = 1 V; f = 1 MHz; T _j = 25 °C	-	1150	-	pF
		V _R = 800 V; f = 1 MHz; T _j = 25 °C	-	70	-	pF
Q_C	total capacitive charge	$V_R = 800 \text{ V}; \text{ dI}_F/\text{dt} = 200 \text{ A/}\mu\text{s}; \text{ I}_F = 20 \text{ A}; $ $T_j = 25 ^{\circ}\text{C}$	-	85	-	nC



(1)
$$T_j = 175 \,^{\circ}C$$

$$(2) T_i = 150 °C$$

(2)
$$T_j = 150 \,^{\circ}\text{C}$$

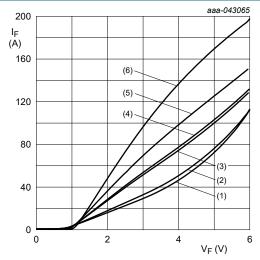
(3) $T_j = 125 \,^{\circ}\text{C}$

$$(3) T_j = 123 C$$

$$(4)$$
 $T_j^{\prime} = 100 \,^{\circ}\text{C}$
(5) $T_j = 25 \,^{\circ}\text{C}$

(6)
$$T_i = -55 \,^{\circ}\text{C}$$

Forward current as a function of forward Fig. 4. voltage; typical values



Pulsed condition: $t_p = 100 \mu s$

(1)
$$T_j = 175$$
 °C

(2)
$$T_i = 150 \, ^{\circ}\text{C}$$

(2)
$$T_j = 150 \,^{\circ}\text{C}$$

(3) $T_j = 125 \,^{\circ}\text{C}$

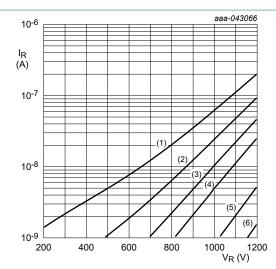
$$(4) T_j = 100 °C$$

$$(5) T_j = 25 °C$$

(6)
$$T_j = -55 \,^{\circ}\text{C}$$

Forward characteristics in surge current as a Fig. 5. function of forward voltage; typical values

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Pulsed condition:

(1) $T_i = 175 \,^{\circ}C$

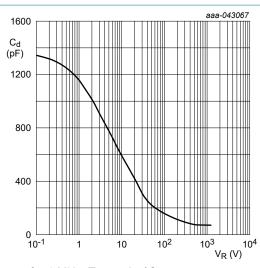
 $(2) T_i = 150 °C$

 $(3) T_i = 125 °C$

 $(4) T_j = 100 °C$

(5) $T_j = 25 ^{\circ}C$ (6) $T_j = -55 ^{\circ}C$

Fig. 6. Reverse current as a function of reverse voltage; typical values



 $f = 1 MHz; T_{amb} = 25 °C$

Fig. 7. Diode capacitance as a function of reverse voltage; typical values

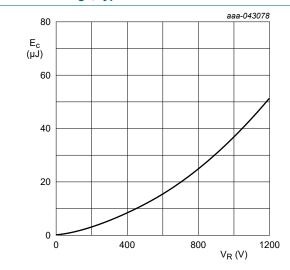


Fig. 8. Capacitance stored energy as a function of reverse voltage; typical values

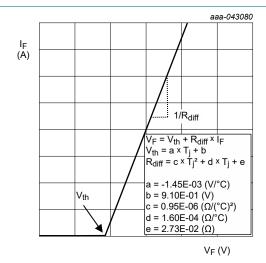
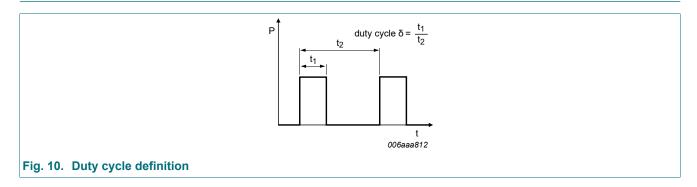


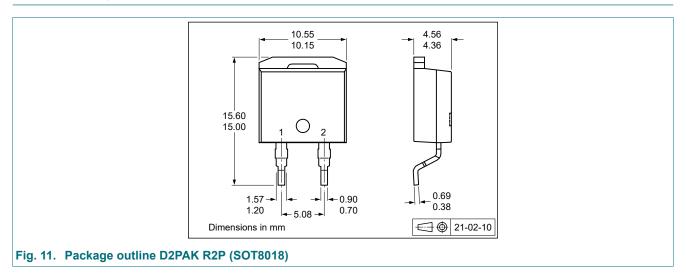
Fig. 9. Simplified forward characteristics mode

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11. Test information

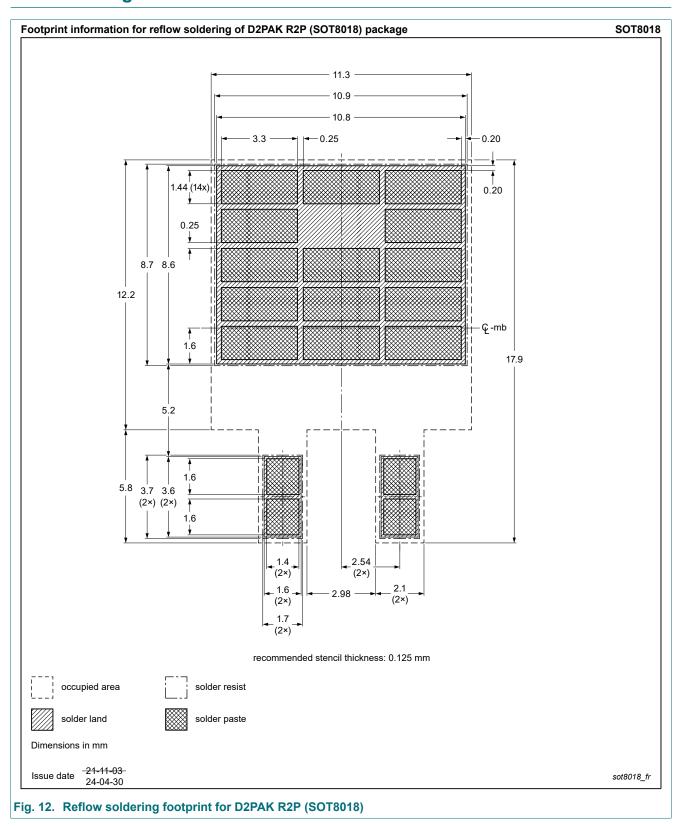


12. Package outline



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13. Soldering



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14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PSC20120J v.1	20250430	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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